

ECE 3550 P#2 Bonus Demos

Student Name	6 char custom ID

Comments

- arkit is fully assembled :
- config file is downloaded :
- SBE is operational :

Tests

Mode 0 Output

- load CR.0=0, CR.1=0, CR.2=0 (CE*=0, AO=1, WR* $\overline{\text{L}}\overline{\text{F}}$)
- write data to peripheral
 CE*=0, AO=0, WR* $\overline{\text{L}}\overline{\text{F}}$ D7-D0: 0x5A, P7-P0:
 then again D7-D0: 0xA5, P7-P0:

Mode 1 output

- load CR.0=1, CR.1=1, CR.2=0 (CE*=0, AO=1, WR* $\overline{\text{L}}\overline{\text{F}}$)
- read status (CE*=0, AO=1, RD* $\overline{\text{L}}\overline{\text{F}}$ → D3-D0 = 1010)
- write data to peripheral
 CE*=0, AO=0, D7-D0=0x3C, WR* $\overline{\text{L}}\overline{\text{F}}$

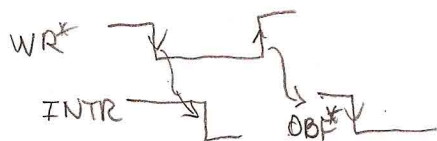
- read status again D3-D0 = 0010

(OBF* shouldn't change)

- assert ACIC*



- read status D3-D0 = 1110
- write new data D7-D0 = 0x3C



- apply RESET $\overline{\text{L}}\overline{\text{F}}$

Mode 1 input (INTE=1) (using the extended CR & SR registers, repeat test from Basic Project #2)