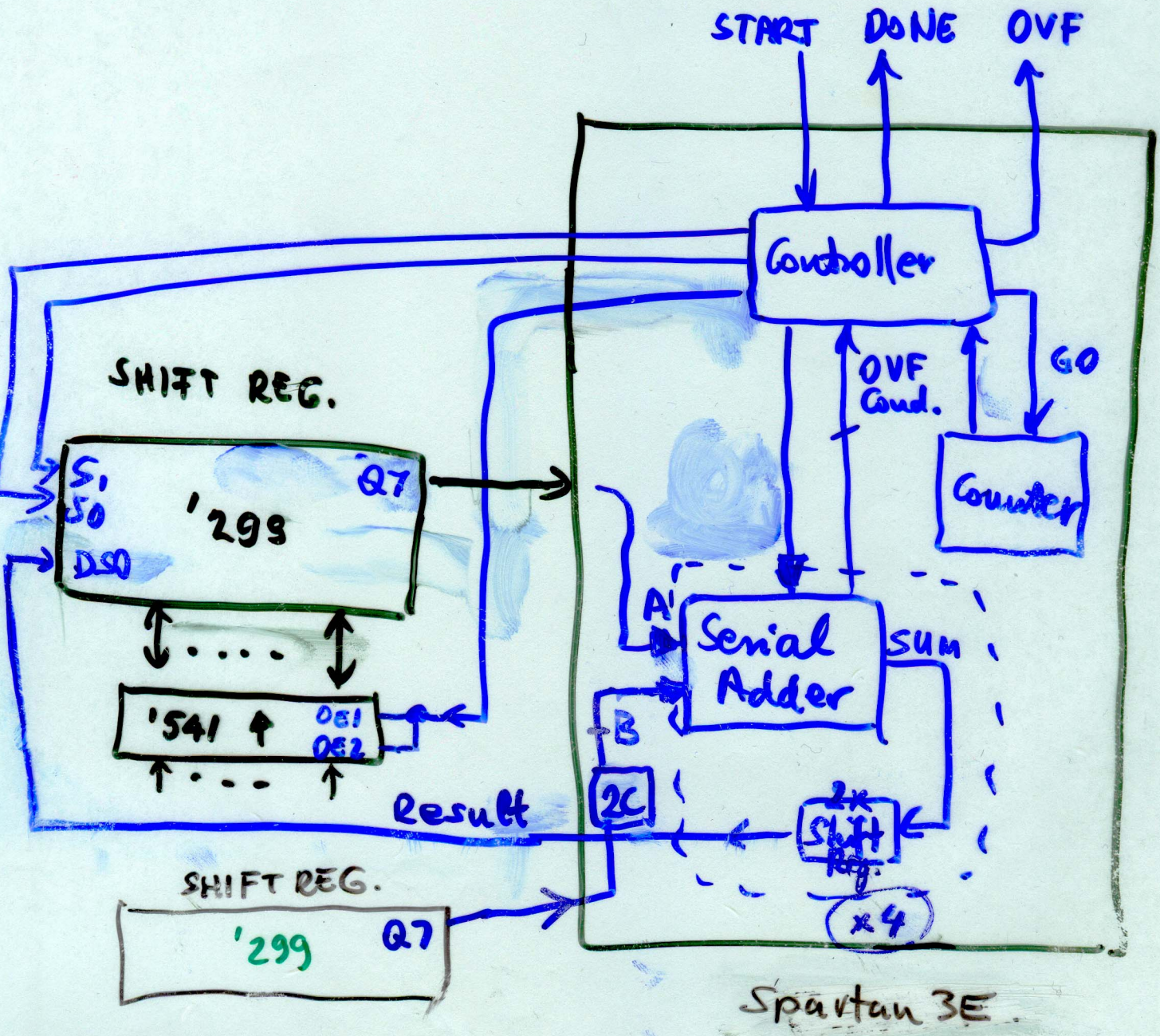


P#1 ARCHITECTURE

CLK

(A possible approach)



HINTS: - First develop a detailed timing diagram on graph paper for the key signals.
 - Practice/familiarize with the shift register, and other circuits