

ECE 4500/ECE 5540 DIGITAL ELECTRONICS
FALL 2011 COURSE OUTLINE
(Preliminary-may be adjusted)

Lecture Topic	Reading
Introduction, technology outlook	
Overview of CMOS transistor models	Ch. 3
The CMOS inverter	Ch. 5
CMOS gate styles	Ch. 6
CMOS logic function blocks	Ch. 11
Sequential circuits	Ch. 7
Sequential circuits and timing issues	Ch. 10
Memory and array structures	Ch. 12
Coping with interconnect	Ch. 4

Project and Exam Dates

Midterm Exam: 3:30 - 4:20pm, Friday, October 14, 2011
ECE 4500 & ECE 5540 Project #1: 5:00pm, Friday, November 4, 2011
ECE 5540 Project #2: 3:00pm, Tuesday, November 22, 2011
Lab Final: 6:30 - 9:20pm, Thursday, December 8, 2011
ECE 4500 Project #2 & ECE 5540 Project #3: 2:00pm, Thursday, December 8, 2011
Final Exam: 12:30 - 2:30pm, Monday, December 12, 2011