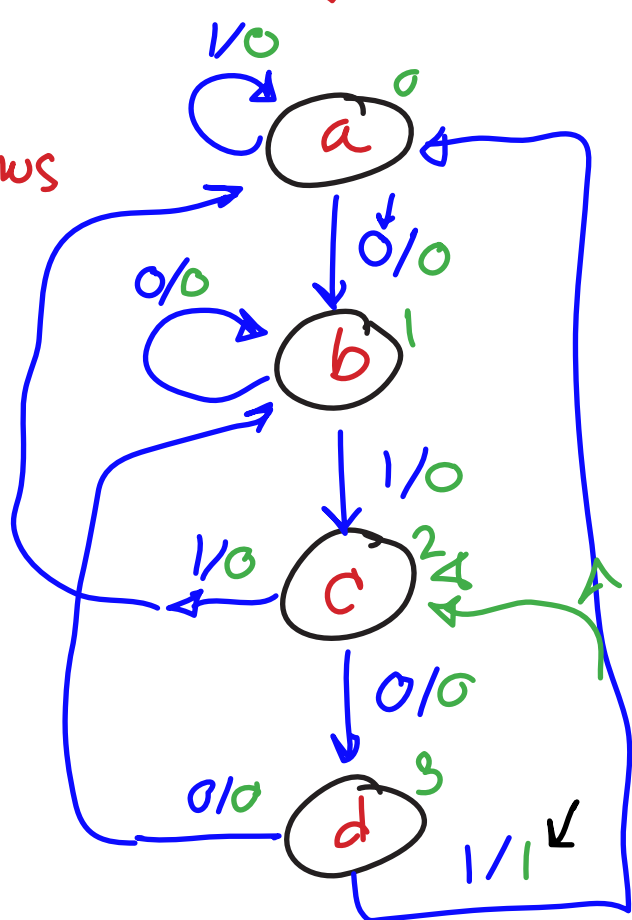


Lab11: Another SLC Design
 5-step procedure
 Similar to Task 1 of Lab1

Step #1 BD:



SD:
 left arrows



right arrow

good 0101
 0101

X = 0 1 0 1 0 1 0 1
 Y = a b c d a b c d
 Z = 0 0 0 1 0 0 0 1
 open lock

Overlap:

0-bits

X = 0111 0111

Right arrow
 gets

a

1-bit

X = 1001 1001

Gets to b

2-bits

X = 0101 0101

Gets to c

3-bits

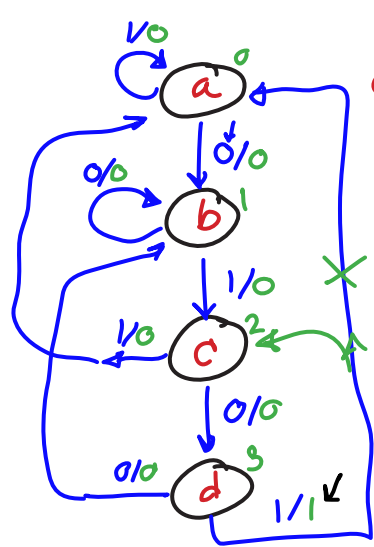
X = 1111 1111

Gets to d

Step #2

State Diagram:

State Table:



8 arrows

D#1	D#2	Y1	Y2	X
a → 00	→ 00	0	0	0
a → 00	→ 11	0	0	1
b → 01	→ 11	0	1	0
b → 01	→ 01	0	1	1
c → 10	→ 01	1	0	0
c → 10	→ 10	1	0	1
d → 11	→ 10	1	1	0
d → 11	→ 11	1	1	1

2 different state assignments

Design #1			Design #2		
Y1(n+1)	Y2(n+1)	Z	Y1(n+1)	Y2(n+1)	Z
0	0	0	0	0	0
0	0	0	0	0	0
0	1	0	1	0	0
1	0	0	0	0	0
0	0	0	1	1	0
0	0	0	0	1	1
0	1	0	1	1	0
1	0	1	0	1	0

8 rows.