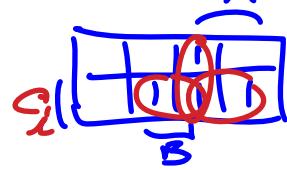


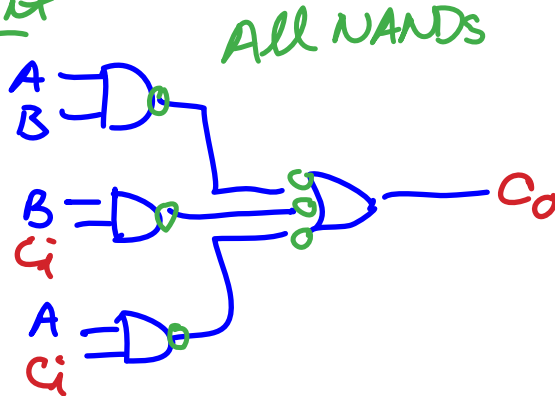
Decoder as SOP function generator; A

Example (C_0 from FA) Lab 5



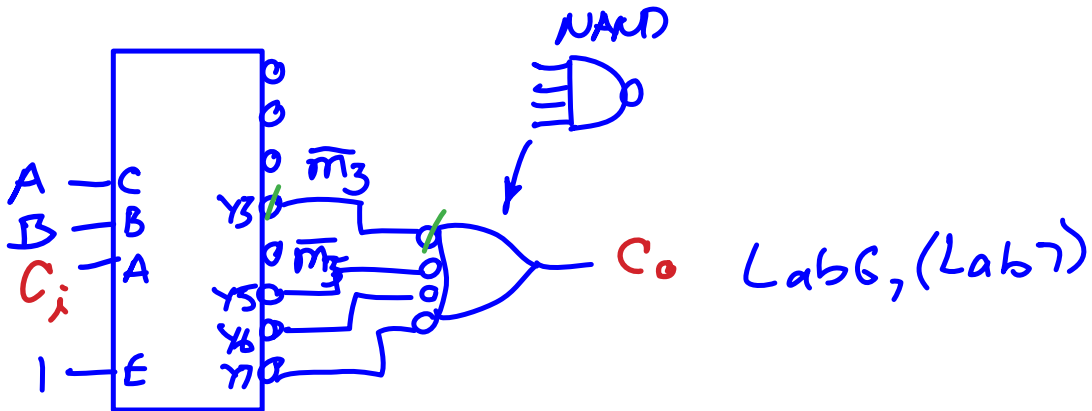
$$C_0 = \sum m(3, 5, 6, 7) = \underline{\underline{AB + BC_i + AC_i}}$$

Circuit



	ABC _i	C ₀
0	000	00
1	001	01
2	010	01
3	011	10
4	100	01
5	101	10
6	110	10
7	111	11

Decoder: $C_0 = \sum m(3, 5, 6, 7)$



MUX

