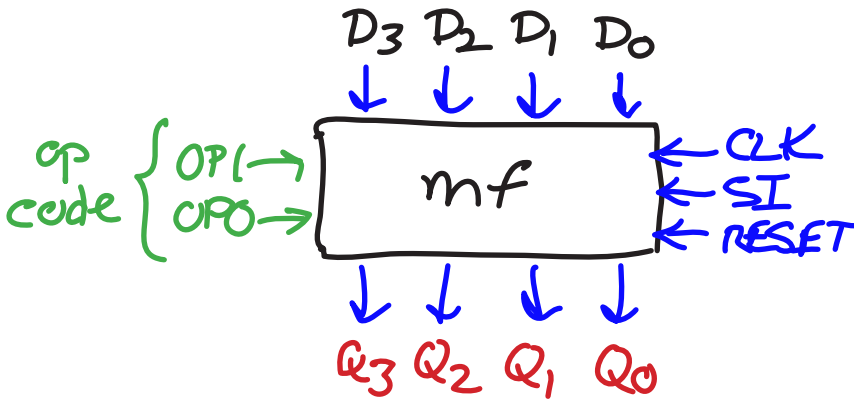


Lab 12 Multifunction Register

(4bit example shown below)

Block Diagram:



Functions

OPI	OPO	Function
0	0	NC
0	1	SR
1	0	SL
1	1	PL

Lab 11, 12
See below

Schematic (4bits, 4 operations only)

