ECE 2100 Circuit Analysis Summer I 2019 Final Exam

NAME: _____

INSTRUCTIONS:

- 1. **THIS EXAM IS CLOSED BOOK AND CLOSED NOTES**. A "Potentially Useful Facts" sheet is provided.
- 2. NO ELECTRONIC DEVICES ARE ALLOWED. All electronic devices, *including watches*, must be stowed away.
- You may only use the provided pencil.
 All other writing instruments and erasers must be stowed away.
- 4. No hats or hoods may be worn during the exam.
- 5. Work each problem in the provided space.
- 6. **Show ALL work** required to arrive at a solution for either full or partial credit.
- 7. READ the entire question before answering.
- 8. Have your student ID on your desktop for inspection by the instructor.
- 9. SIGN the honesty pledge at the bottom of the page. Exams without a signature will receive no credit.

I have neither given nor received assistance from anyone in regards to completion of this exam. I have followed the instructions as provided on this sheet. I HAVE VERIFIED THAT THIS EXAM HAS (7) PAGES.

SIGNATURE: _____ DATE: _____

Note: Schematics prepared using LTspice (linear.com).

Potentially Useful Facts (updated 6 January 2017)

1.
$$A \angle \theta = A e^{j\theta} = A \cos \theta + A \sin \theta j$$

2.
$$v = L \frac{dt}{dt}$$
 (follows passive sign convention)

3.
$$i = C \frac{dv}{dt}$$
 (follows passive sign convention)

4.
$$\overrightarrow{\mathbf{Z}_{L}} = j\omega l$$

5.
$$\overline{Z_c} = \frac{1}{j\omega C}$$

6.
$$\vec{S} = \vec{V_{\text{RMS}}} (\vec{I_{\text{RMS}}})^*$$
 (follows passive sign convention)

7.
$$V_{\rm RMS=} \sqrt{\frac{1}{T} \int_0^T v^2(t) dt}$$



source (released to public domain): https://commons.wikimedia.org/wiki/File:45-45-triangle.svg



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8.

9.

Maximum exam score is 27 points.

1. (5 points) The op-amp is ideal.

Find the Thevenin input resistance of the op-amp circuit, that is, the ratio Vx/Ix.



THERE ARE TWO PROBLEMS ON THIS PAGE

2. (5 points) The complex power of a load is $\vec{S}=1$ - j VA. If the load voltage is 1V RMS and the frequency is 1 rad/s, find the value of a component to put in parallel with the load so that the new load has a unity power factor.

Your work must be clear – as always, watch units!

3. (2 points) Consider the system

$$y(t) = T[x(t)] = \frac{dx(t)}{dt}$$

where x(t) is the time-varying system input and y(t) is the system output. Is this system linear? **Justify your response**. No partial credit. 4. (5 points) Find node voltage v(t) in the sinusoidal steady state using the **superposition principle**.



5. (5 points) Find the power of each circuit element.You **must** show the voltage across (with polarity) and current through (with direction) each element. Put answers in table.



6. (5 points). The switch is closed at t=0.Find v(t) for t>=0 assuming that the capacitor is uncharged at t=0.

